

VIA FIRST CLASS US MAIL

BUR920000141US1

Docket No. IBMB110321000 Date 11/28/05 By JJT:cmt

Serial No. 10/798,816 Reg/Pat No. _____

In Re: CLEANING OF SEMICONDUCTOR WAFERS BY ...

The following was received in the U.S. Patent Office Mail Room on the date stamped hereon:

- | | |
|--|--|
| <input type="checkbox"/> Application for Patent Including
<u> </u> Pages Spec., <u> </u> Pages Claims
<u> </u> Abstract | <input type="checkbox"/> Appln. for TM Reg., <u> </u> Specimens |
| <input type="checkbox"/> Declaration/Power of Attorney | <input type="checkbox"/> Combined § 8 & 15 |
| <input type="checkbox"/> Assignment/Cover Sheet | <input type="checkbox"/> Combined § 8 & 9 |
| <input type="checkbox"/> IDS/Form PTO1449 | <input type="checkbox"/> Statement of Use |
| <input type="checkbox"/> EFS Form | <input type="checkbox"/> Request for Extension of Time |
| <input type="checkbox"/> Small Entity Declaration | <input type="checkbox"/> Amendment |
| <input type="checkbox"/> Drawings <u> </u> Sheet(s) | <input type="checkbox"/> Brief |
| <input type="checkbox"/> Non Publication Request | <input type="checkbox"/> Petition <u> </u> |
| <input type="checkbox"/> Request <u> </u> | <input type="checkbox"/> Notice of Appeal |
| <input type="checkbox"/> Letter <u> </u> | <input type="checkbox"/> Issue Fee Transmittal |
| <input type="checkbox"/> Check No. <u> </u> for \$ <u> </u> | <input type="checkbox"/> <u> </u> Maintenance Fee |
| | <input checked="" type="checkbox"/> <u>Resp. to Restriction</u> |
| | Requirement |

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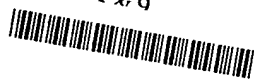
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| <input type="checkbox"/> IDS/Form PTO1449 | <input type="checkbox"/> Change of Fee Address |
| <input type="checkbox"/> EFS Form | <input checked="" type="checkbox"/> <u>Resp. to Restriction</u> |
| <input type="checkbox"/> Small Entity Declaration | Requirement |
| <input type="checkbox"/> Drawings <u> </u> Sheet(s) | |
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| <input type="checkbox"/> Letter <u> </u> | |
| <input type="checkbox"/> Check No. <u> </u> for \$ <u> </u> | |



12-01-2005

U.S. Patent & TMO/c/TM Mail Rcpt Dt. #72



IFW

DOCKET: BUR920000141US2

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR:	Nicole S. Carpenter et al.)	EXAMINER:	Mahmoud Dahimene
)		
SERIAL NO.:	10/798,816)	ART UNIT:	1765
)		
FILING DATE:	March 10, 2004)	DATE:	April 19, 2006
)		
FOR:	CLEANING OF SEMICONDUCTOR WAFERS BY CONTAMINATE ENCAPSULATION)		

**LETTER REGARDING RESPONSE TO RESTRICTION REQUIREMENT
WHICH WAS TIMELY MAILED TO THE UNITED STATES PATENT AND
TRADEMARK OFFICE AND RECEIVED BY THE OFFICE BUT NOT ENTERED
INTO THE FILE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

A Restriction Requirement Office Action was received dated November 2, 2005 for the subject application and a Response to Restriction Requirement was timely mailed to the United States Patent and Trademark Office (USPTO) on November 28, 2005.

Upon our regular docket review of the status of applications, it was uncovered that there was no Office Action since the restriction requirement of November 2, 2005. The

undersigned attorney called the Examiner on April 17, 2006 and was advised that the Examiner would check on the status of the application. The undersigned received a call from the Examiner on April 19 indicating that there is no record of the Response in the file.

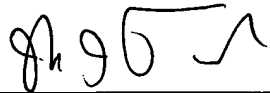
The Response to Restriction Requirement (copy enclosed) was timely filed and was mailed to the USPTO under 37 CFR 1.8. Also enclosed is a copy of the acknowledge post card showing that the Response was received on December 1, 2005 by the United States Patent and Trademark Office.

The undersigned attorney attests on personal knowledge that the Response to Restriction Requirement was timely mailed to the United States Patent and Trademark Office as evidenced by the attached Response and stamped acknowledgement post card.

Applicants respectfully submit that the enclosed copy of the Response to Restriction Requirement be entered and the subject application be examined.

Further and favorable action is hereby requested.

Respectfully submitted,

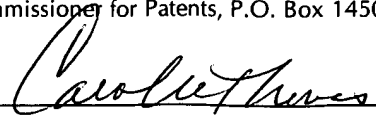


John J. Tomaszewski
Reg. No. 26,241

DeLIO & PETERSON, LLC
121 Whitney Avenue
New Haven, CT 06510-1241
(203) 787-0595

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date indicated below as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 2231301450.

Name: Carol M. Thomas Date: April 19, 2006 Signature: 
ibmb110321000Letter-To-PTO



DOCKET: BUR920000141US2

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR:	Nicole S. Carpenter et al.)	EXAMINER:	Mahmoud Dahimene
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SERIAL NO.:	10/798,816)	ART UNIT:	1765
)		
FILING DATE:	March 10, 2004)	DATE:	November 28, 2005
)		
FOR:	CLEANING OF)		
	SEMICONDUCTOR)		
	WAFERS BY)		
	CONTAMINATE)		
	ENCAPSULATION)		

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Responsive to the Restriction Requirement Office Action mailed November 2, 2005, Applicants elect the subject matter of Group I, claims 11 and 13-16, for continued examination herein.

REMARKS

The Examiner has restricted the invention of the subject application to one of the following inventions: